

L Number	Hits	Search Text	DB	Time stamp
2	44756	((integrated near2 circuit) or VLSI or circuit or IC) and design\$3 and synthesi\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/12 10:04
3	1237	(gate-level or (gate near1 level)) and (((integrated near2 circuit) or VLSI or circuit or IC) and design\$3 and synthesi\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/12 10:05
4	69	((binary adj1 decision adj1 diagram) or BDD) and ((gate-level or (gate near1 level)) and (((integrated near2 circuit) or VLSI or circuit or IC) and design\$3 and synthesi\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/12 10:21
5	28432	(resource near3 shar\$3) or (operator near3 order) or (operator near3 merg\$3) and (((integrated near2 circuit) or VLSI or circuit or IC) and design\$3 and synthesi\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/12 10:25
6	1	((binary adj1 decision adj1 diagram) or BDD) and ((gate-level or (gate near1 level)) and (((integrated near2 circuit) or VLSI or circuit or IC) and design\$3 and synthesi\$4)) and ((resource near3 shar\$3) or (operator near3 order) or (operator near3 merg\$3) and (((integrated near2 circuit) or VLSI or circuit or IC) and design\$3 and synthesi\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/12 10:25
7	14	multiplier and ((binary adj1 decision adj1 diagram) or BDD) and ((gate-level or (gate near1 level)) and (((integrated near2 circuit) or VLSI or circuit or IC) and design\$3 and synthesi\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/12/12 10:59